

FIGURE 1

100a

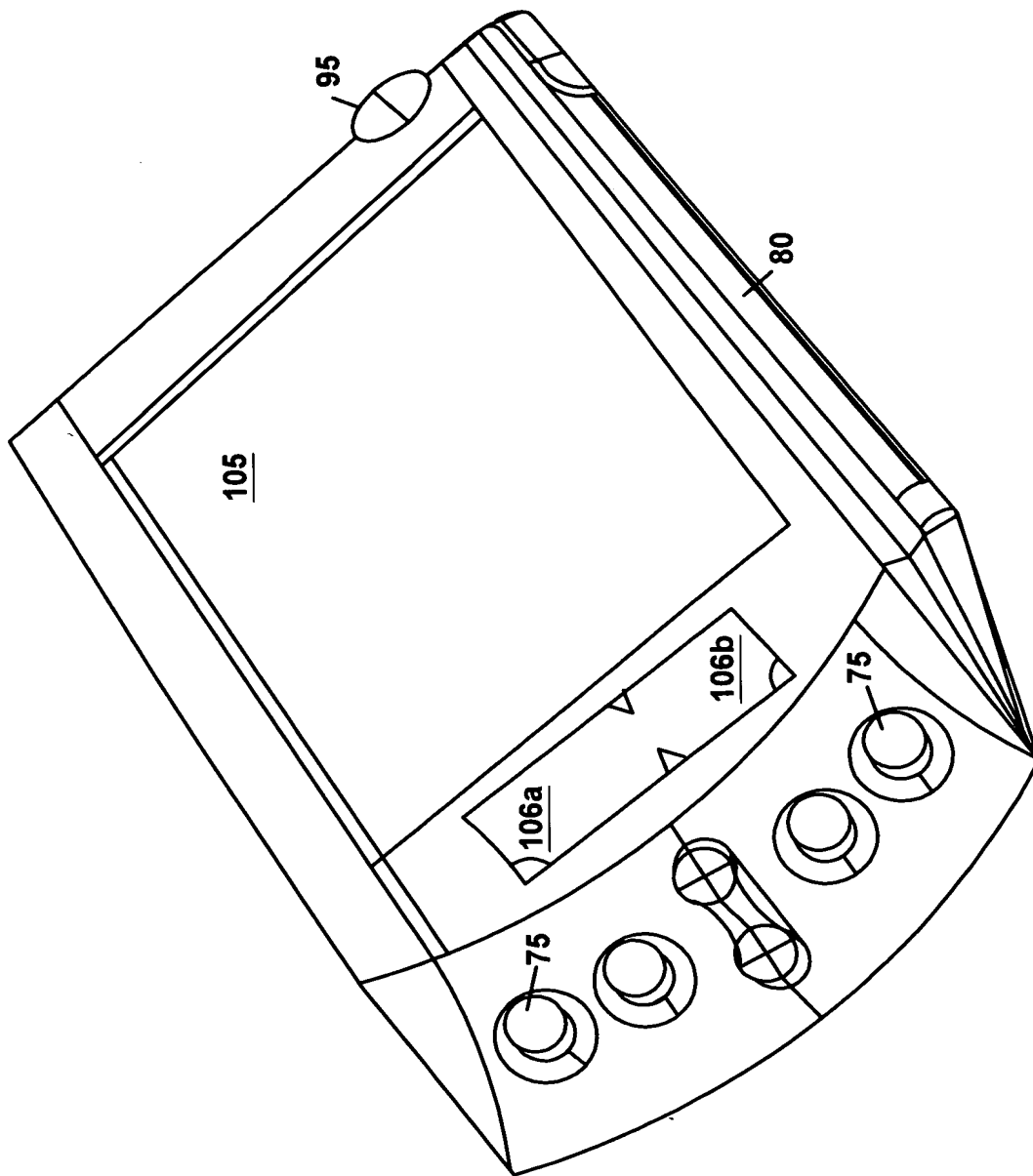


FIGURE 2

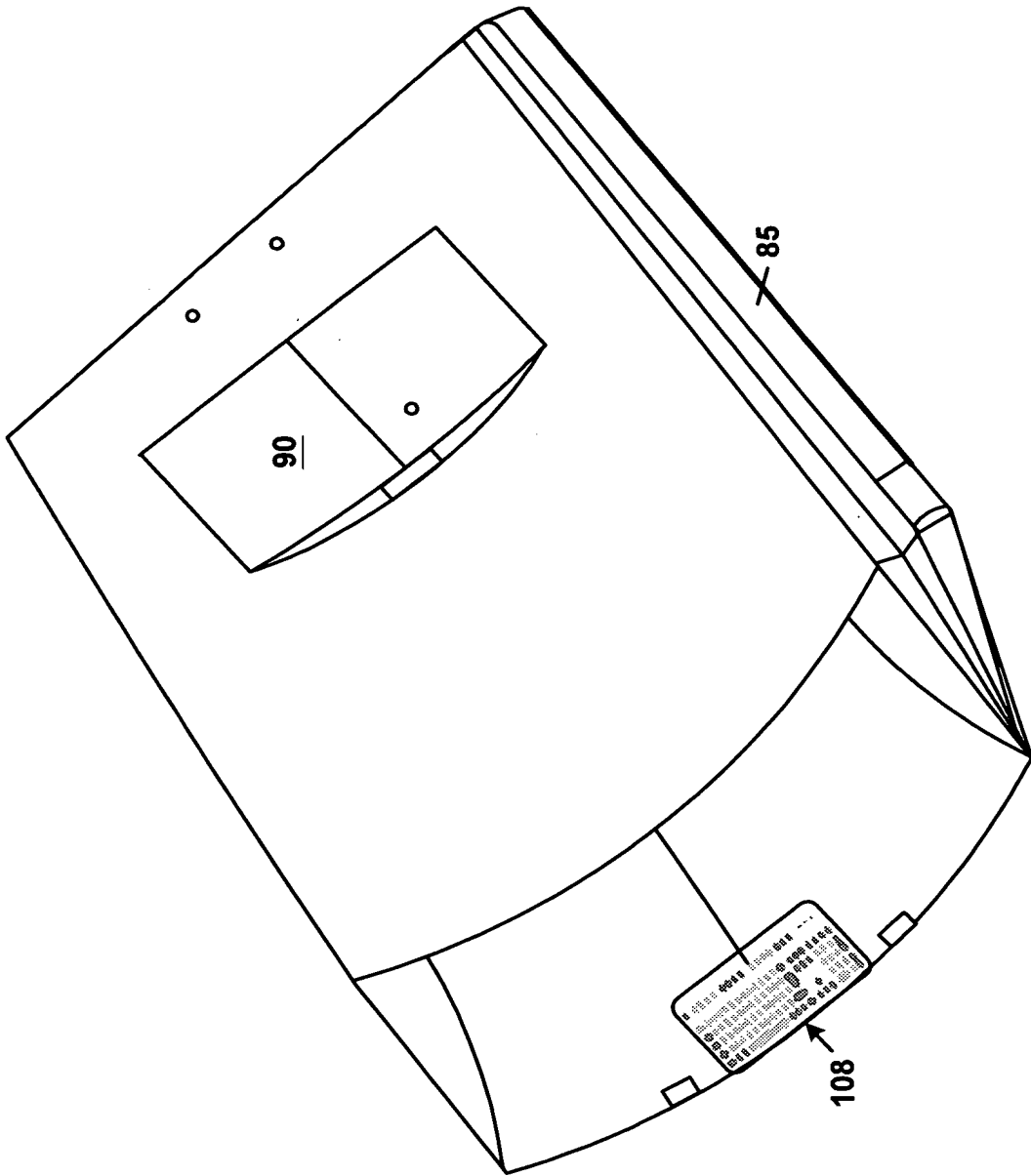


FIGURE 3

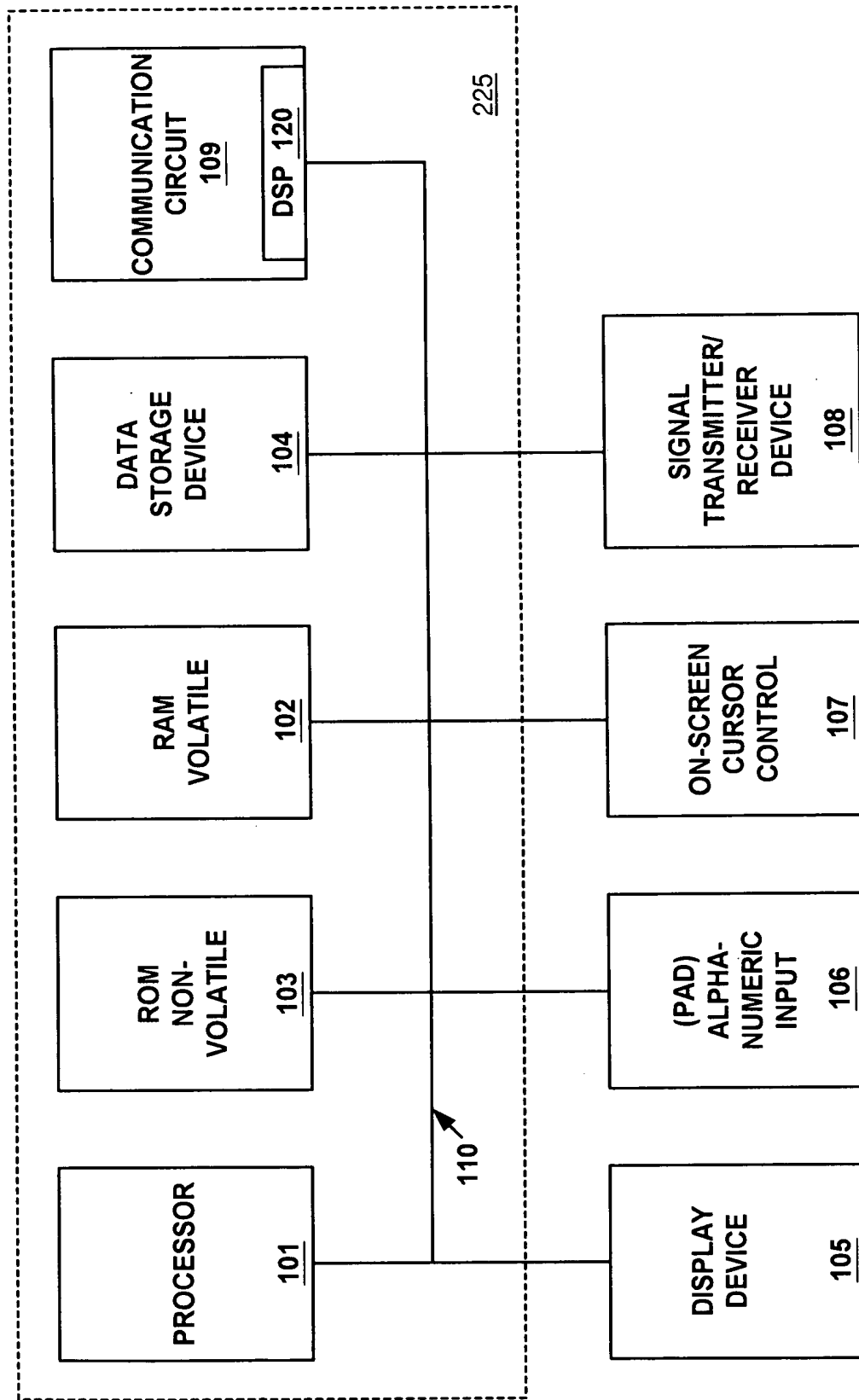


FIGURE 4

500A**MOBITEX FRAME HEADER**

	7	6	5	4	3	2	1	0
BYTE 0	BIT SYNCH 1 <u>501</u>							
BYTE 1	BIT SYNCH 2 <u>502</u>							
BYTE 2	FRAME SYNCH 1 <u>503</u>							
BYTE 3	FRAME SYNCH 2 <u>504</u>							
BYTE 4	CONTROL BYTE 1 <u>505A</u>							
BYTE 5	CONTROL BYTE 2 <u>506A</u>							
BYTE 6	FEC 1 (P) <u>507</u>				FEC 2 (Q) <u>508</u>			

FIGURE 5A

500B

HEADER CONTROL BYTES AND FEC								
	7	6	5	4	3	2	1	0
BYTE 4	BASE ID <u>505B1</u>						AREA ID.... <u>505B2</u>	
BYTE 5AREA ID <u>506B1</u>				CONTROL FLAGS <u>506B2</u>			
BYTE 6	FEC 1 (P) <u>507</u>				FEC 2 (Q) <u>508</u>			

FIGURE 5B

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

FIGURE 6A

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1
<u>699.p1</u>										

FIGURE 6B

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1

699.p2

FIGURE 6C

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1
<u>699.p3</u>					■	■	■			

FIGURE 6D

600

A5	C0	C1	C2	C3	P0	P3	Q0	Q1	Q2	Q3
1	0	0	0	0	1	1	1	0	0	1
								<u>699.p9</u>		

FIGURE 6E

700

12/13

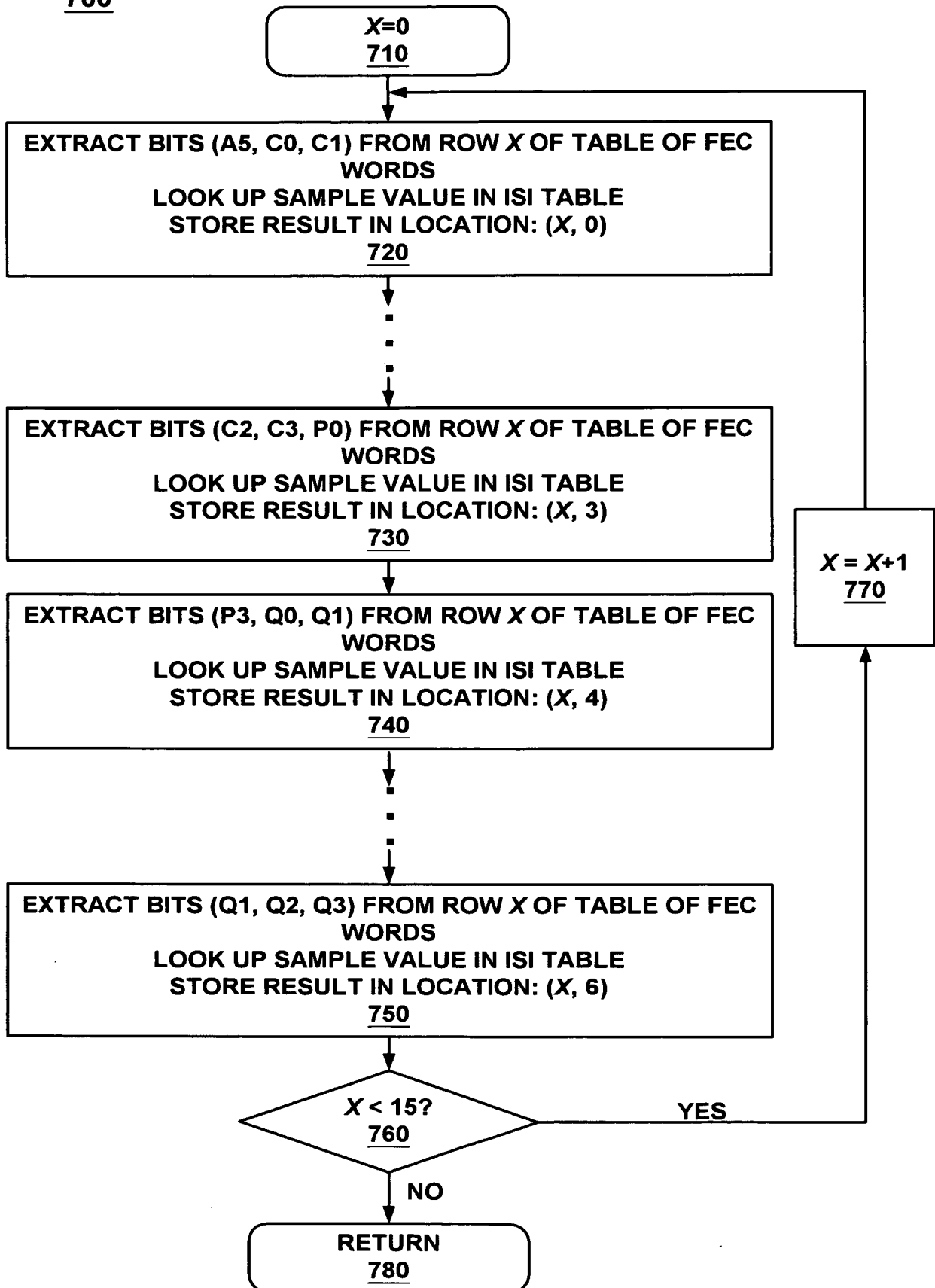
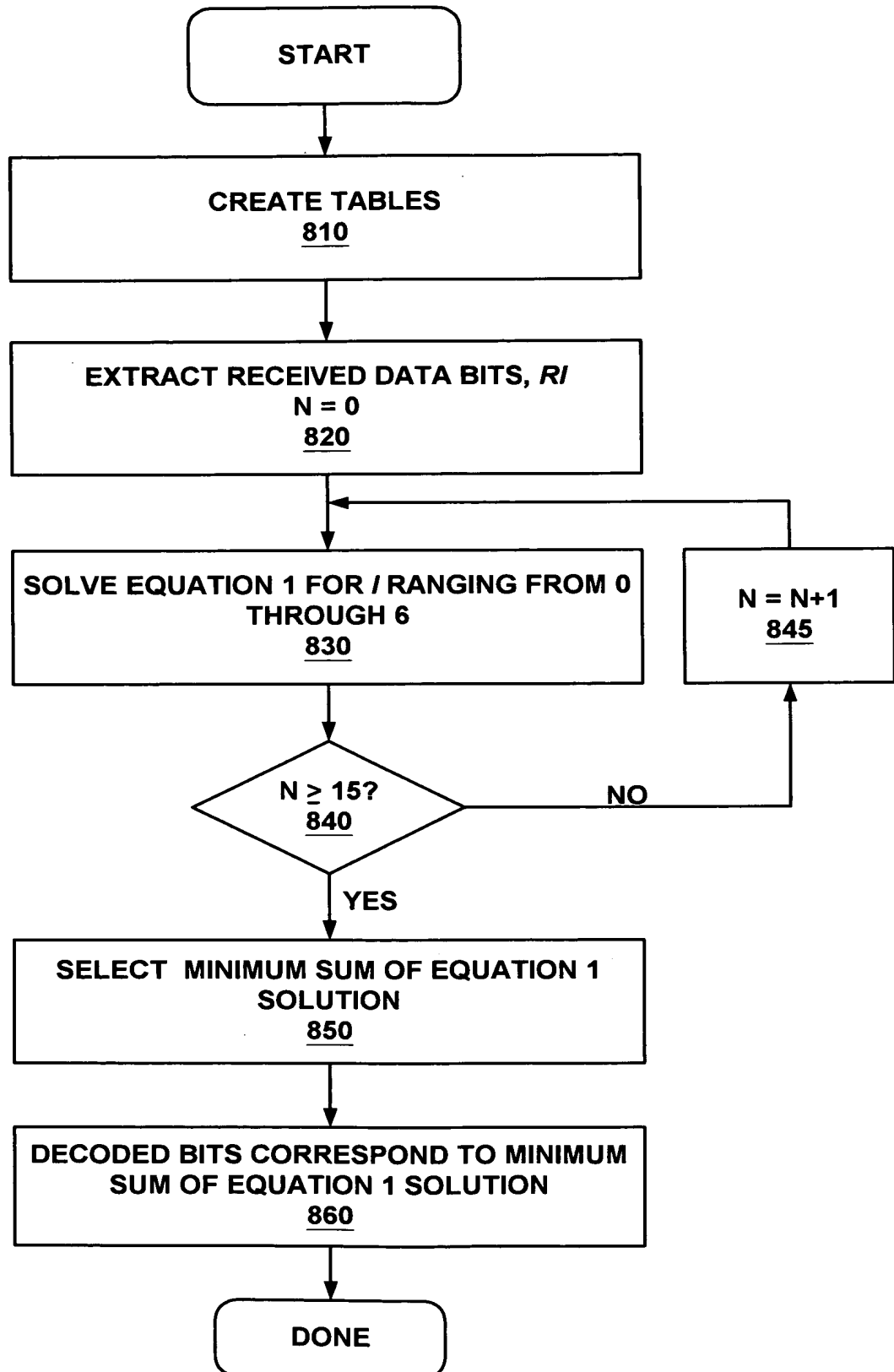


FIGURE 7

**FIGURE 8**